

Electronic Acknowledgement Receipt

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Application Number:	10774890
International Application Number:	
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Title of Invention:	RELAXED SIGE PLATFORM FOR HIGH SPEED CMOS ELECTRONICS AND HIGH SPEED ANALOG CIRCUITS
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement Letter	SIDS.pdf	64346	no	2
			2050e68f8cd12513074da94e17f2439f0f514602		

Warnings:**Information:**

2	Information Disclosure Statement (IDS) Filed	ASC049C1PTOSB08.pdf	78764	no	1
			0722a311056bb896e9f822033acc414f7d9a52ac		

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3	NPL Documents	C1.PDF	2650267	no	48
			7164fd8ff3b61cb43f28cdea03cb24d5dc577603		

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4	Fee Worksheet (PTO-06)	fee-info.pdf	8230	no	2
			8ebac55c08d7268660a44354ec57d19e1c17a58d		

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Total Files Size (in bytes):	2801607
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New International Application Filed with the USPTO as a Receiving Office

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